



# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/646,922	08/25/2003	Masami Makino	031057	1951
23850	7590	06/27/2007	EXAMINER	
ARMSTRONG, KRATZ, QUINTOS, HANSON & BROOKS, LLP			SAMS, MATTHEW C	
1725 K STREET, NW			ART UNIT	PAPER NUMBER
SUITE 1000			2617	
WASHINGTON, DC 20006				
MAIL DATE		DELIVERY MODE		
06/27/2007		PAPER		

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Advisory Action Before the Filing of an Appeal Brief</b>	Application No.	Applicant(s)
	10/646,922	MAKINO ET AL.
Examiner	Art Unit	
Matthew C. Sams	2617	

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 11 June 2007 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1.  The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

a)  The period for reply expires 3 months from the mailing date of the final rejection.

b)  The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.

Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**NOTICE OF APPEAL**

2.  The Notice of Appeal was filed on \_\_\_\_\_. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

**AMENDMENTS**

3.  The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because

(a)  They raise new issues that would require further consideration and/or search (see NOTE below);

(b)  They raise the issue of new matter (see NOTE below);

(c)  They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or

(d)  They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: \_\_\_\_\_. (See 37 CFR 1.116 and 41.33(a)).

4.  The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).

5.  Applicant's reply has overcome the following rejection(s): \_\_\_\_\_.

6.  Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).

7.  For purposes of appeal, the proposed amendment(s): a)  will not be entered, or b)  will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: \_\_\_\_\_

Claim(s) objected to: \_\_\_\_\_

Claim(s) rejected: \_\_\_\_\_

Claim(s) withdrawn from consideration: \_\_\_\_\_

**AFFIDAVIT OR OTHER EVIDENCE**

8.  The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).

9.  The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing of good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).

10.  The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

**REQUEST FOR RECONSIDERATION/OTHER**

11.  The request for reconsideration has been considered but does NOT place the application in condition for allowance because:  
See Continuation Sheet.

12.  Note the attached Information Disclosure Statement(s). (PTO/SB/08) Paper No(s). \_\_\_\_\_

13.  Other: \_\_\_\_\_

Continuation of 11. does NOT place the application in condition for allowance because: The applicant's arguments are not persuasive. Kweon shows in (Fig. 3 [LCD 27]) a chip between two mounting holes that is located on a second parallel plane above the LCD [27]. Kweon teaches the same alignment of two mounting holes, but with a hole in place of the chip in (Fig. 3 [25]). Kweon teaches the light guide plate (Fig. 3 [23]) can clearly be inserted into the frame (Fig. 3 [24]) as seen by the contour along the inside of the frame. (Col. 3 lines 59-60) Kweon teaches the combination of [23 & 24] is inserted into the LCD [27], between the microchip with mounting holes and the LCD [27]. (Col. 3 lines 59-67) Kweon teaches the PCB [25] mounts to the frame [24] through the holes that are in the respective top left and right corners. Therefore, when the second LCD [28] is attached to the PCB [25], it would be obvious to one of ordinary skill in the art to use the teachings of JP 59-78654 to stagger the mounting locations of the chips to make the configuration thinner because the chips are directly next to each other in an opening of a frame.

Upon a closer review of Kweon Fig. 5, the examiner notes LCD 27 appears wider than Frame 24. A more favorable review could include an amendment to the independent claims which clearly explains that both LCDs are embedded within and surrounded by the frame, which would differentiate from Kweon.



LESTER G. KINCAID  
SUPERVISORY PRIMARY EXAMINER